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# 如何在 WP-5238-CE7 PAC 內使用 XV-Board?

下載 FAQ-010 範例

XV board 是屬於 Modbus Slave I/O 板卡,使用前,需先將此板卡插在 WP-5238-CE7 內,並在 Win-GRAF Workbench 中啟用 WP-5238-CE7 為 Modbus Master。您可在 XV board 選型網頁內, 查詢詳細的規格資訊: http://www.icpdas.com/en/product/guide+PACs+WinCE\_\_PACs+WP-5000-CE7\_WP-5000#967。可在出貨光碟中 (CD-ROM:Napdos\Win-GRAF\demo-project\) 或 網站下載

以下的 Win-GRAF 範例程式。

https://www.icpdas.com/en/download/show.php?num=712&nation=US&kind1=&model=&kw=win-graf

範例	檔名	說明
<u>XV107, XV107A</u>	demo_XV107.zip	讀取 8 DI · 寫出 8 DO
<u>XV110</u>	demo_XV110.zip	讀取 16 DI
<u>XV111, XV111A</u>	demo_XV111.zip	寫出 16 DO · 讀取 1 DO
<u>XV116</u>	demo_XV116.zip	讀取 5 DI · 寫出 6 D0
XV308 1 XV308 2 XV308 3	demo_XV308_1.zip demo_XV308_2.zip demo_XV308_3.zip	<ol> <li>1. 讀取 8 AI · 讀取 8 DI</li> <li>2. 讀取 8 AI · 寫出 8 DO</li> <li>3. 讀取 8 AI · 寫出 4 DO · 讀取 4 DI</li> </ol>
<u>XV310</u>	demo_XV310.zip	讀取 4 AI · 寫出 4 DO · 讀取 4 DI · 寫出 4 AO

之後,執行 Win-GRAF 並點選功能表 "File" > "Add Existing Project" > "From Zip",來回存範例程式 並查看詳細的程式內容。

# 1.1. 通用設定說明:

1. 滑鼠點選工具列上的 "Open Fieldbus Configuration" 按鈕來開啟 "I/O Drivers" 視窗。

rue ran yew Insert Project		window Helb	<b>III A 16</b>		ja 🗳 🎽	ا 🖪 🛓					
Workspace	10 0	)rivers *		<u> </u>						HZ	4X
⊡ 🗊 demo_XV107	E	🖃 📲 MODBUS Master						🝸 Nar	ne	Туре	
🗄 🔤 Exception programs	2	由 _ 品 RTU: COM0:11	15200,N,8,1					X	/107 DI 00	BOOL	~
🛓 🔤 Programs			Input Bits (1) [18]				_	X	/107 DI 01	BOOL	
I Main		🗄 <b>* 🗐</b> <15> Write	Coil Bits (1) [18]					X	/107 DL 02	BOOL	
	0	Ma MODBUS Slave	1996 S					X	/107 DL 03	BOOL	
Soft Scope	B	1						X	/107 DL 04	BOOL	
		Request	Slave/Unit	Address	Nb Item	Activation	Period (ms)	X	/107 DI 05	BOOL	
📲 🚮 Binding Configuration		<2> Read Input Bits	1	1	8	Periodic	50	<			2
🚽 🕺 🚽 Global defines	¢'s	<15> Write Coil Bits	1	1	8	On Change	0	Mama	1.4	(alua	-
🚮 Variables								Name		alue	
E Types	₿†	<					>	<			>
	Bui	ld									X

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<u>注意:</u> 上述所 固定語	所列的範例中, 設定為 "COMO MODBUS	皆啟用 \ ):115200, S Master Por	WP-5238-CE7 N,8,1″°	為 Modb	ous <b>RTU</b> Maste	er · 且 "Con	n. Port" 需
	O MO Ado Pro	DBUS on Ether dress: t: tocol: 請 "As	met 為 Modbus AS 改為填入 SCII:COM0:115	CII Maste	OK Cancel er · 1″		
	© <u>Ser</u> ⊆or Delay b <u>D</u> el	ial MODBUS-R n. port: CO between reque ay (ms): 0	TU M0:115200,N,8,1 sts				
<u>規劃 AI/AO 通数</u> 若要在 WP-5238 先暫停 Win-GRA <u>以 WP-5238 為</u> 1. 點選 "Win_G Driver"。	<u>道</u> 8-CE7 内使用 X AF Driver · 再使 <u>例來說明:</u> 6RAF_WP_5238"	V Board ( 用 "DCON (或下方)	例如: XV308, X N_Utility_Pro_( 的小圖示) 開刷	(V310) CE_200.e	勺 AI/AO 通道 xe" 來規劃每 GRAF Driver 衫	፬,使用前需 葺個 AI/AO ᢃ 見窗.並點選	聲在 PAC 內 通道。 ፪ "End
My Device R Internet Explorer Isqlw35 Wicrosoft WordPad My Documents PAC_Utility Virt	egEdit manager GRAF p=5238 vrdows Embedded Comp	Win-GRAF-WP WP-5238 This prod Project n Elapsed ti	-5238 driver Version 1.01 , Ju uct is licensed. ame : demo_XV110 , ime : 0. 0: 0:19	ul. 16, 2014 VMDB size : 30	D8928 End Driver 停用 Win-	OK GRAF Driver	

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2. 點選桌面上的 "DCON_Utilit <u>File Edit</u> Addres: \Sys auto_config colog_report CE7_Platform Platform.dll	<ol> <li>點選桌面上的 "My Device" 並在 \System_Disk\Tools\dcon_utility_pro 路徑下,點選 "DCON_Utility_Pro_CE_200.exe" 開啟設定視窗。</li> <li>File Edit View Go Favorites ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) ( ) (</li></ol>								
3. 點選 COM Port 按鈕開啟設定視窗,接著 "COM Port" 固定設為 "COM0","Baud Rate" 固定 設為 115200, "Format" 固定為 "N,8,1",再按 "OK"。									
DCN Odly P0 2.0.01 00 (2) 200 (10 (2) 200)         Start Address       0         ED Addr       Baud Rate         ID       Addr         Baud Rate       Checks         Format       5t         COMPort       Timeout         COMO[Backplane]       200         Main       0         N,8,1       0         N,8,1       0         OK       Cancel									
4. 點選搜尋( 設定視窗。	▶) 按鈕後,將 DCON Utility Pro 「「」」 Start Address ID Addr XV308 1[1h] 湯鼠雙 COM:0[N,8,1]	列出 PA 2.0.0.0 for CE2 0 En Baud Rate 115200	C 中的 XV Bo	pard (例如 at Status [[	1: XV308) ・ 浄 Description Modbus RTU]8*A	● 鼠雙擊此:	項目來開啟		
		ICP DAS	Co., Ltd. Techn	nical Doci	iment				

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Autr 5. 方 言	NOT 会定畫面中 受定為 "Fast Cor Pro Add Bau Che Da Fast	Janice Hong Janice Hong Node" 並取 B Firmware[A102] figuration AI/AI Ala tocol(INIT*) M dress 1 ud Rate(INIT*) 1 ity(INIT*) N ecksum(INIT*) 0 ta Format Fa	version ormat"設 肖勾選"F rm DO/Alar odbus RTU 15200 8,1-None Pa isable ngineering Fo	1.0.0 定為 "Eng Reverse DI Reverse DI I I I I I I I I I I I I I I I I I I	JDate gineering Fo State (INIT <sup>*</sup>		_Page 定) · 將 "Fa	4 / 25 st Mode"
<b>6.</b> 方 軍	《設定畫面中 月按 "Set Ala	sponse Delay	<u></u> (個 AI 通	道規劃適	Set Mo 當的設定並	odule Configurat 立記得勾選欲使	tions 使用的 AI 通道	道 · 完成後
	Configur	ation AI/AI Alarm	DO/Alarm	Host WDT D	I About			
					High Alarm	Limit Low Alarm I	_imit Alarm M	lode
		0       -00003 [-000.003         11       -00004 [-00.0040         12       -00052 [-00.0052         13       -00011 [-00.0011         14       -00022 [-000.022         15       -00005 [-000.005         16       -00006 [-000.005         17       -00001 [-000.005	3] [08 0] [09 2] [05 1] [0A 2] [0A 2] [0A 5] [08 5] [08 1] [08	] +/- 10 V	<ul> <li>10</li> <li>5</li> <li>2.5</li> <li>1</li> <li>20</li> <li>10</li> <li>10</li> <li>10</li> <li>10</li> <li>10</li> </ul>	-10 -5 -2.5 -1 -20 -10 -10 -10 -10 -10	Disable	
					chnical D-	cumont		
			ICP DAS	CO., LTO. 16	echnical Do	cument		

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## <u>XV308:</u>

Type Code	Range	Data Format	Minimum	Maximum
05		Engineering	-25000	+25000
05	+/-2.5 V	Hexadecimal	8000h	7FFFh
06	1 / 20 m A	Engineering	-20000	+20000
06	+/-20 MA	Hexadecimal	8000h	7FFFh
07	14 m A ~ 120 m A	Engineering	+4000	+20000
07	+4 mA * +20 mA	Hexadecimal	0000h	FFFFh
08	08 +/-10 V	Engineering	-10000	+10000
08		Hexadecimal	8000h	7FFFh
00		Engineering	-5000	+5000
09	+/-5 V	Hexadecimal	8000h	7FFFh
0.0	. / 1 . /	Engineering	-10000	+10000
UA	+/-1 V	Hexadecimal	8000h	7FFFh
00	1 / 20 m A	Engineering	-20000	+20000
UD	+/-20 MA	Hexadecimal	8000h	7FFFh
1 ^	$0 m \Lambda \simeq \pm 20 m \Lambda$	Engineering	0	+20000
IA	0 mA ~ +20 mA	Hexadecimal	0000h	FFFFh

# <u>註:</u>

(1) 資料格式建議使用 "Engineering",較方便辨識數值。

(例如: +/-2.5 V 顯示為 -25000 ~ +25000 · +4 mA ~ +20 mA 顯示為 +4000 ~ +20000)

(2) 若使用的 "Type Code" 為 06,07,0D,1A 需注意 XV Board 上 8 個 Jumper 的位置 是否正確!

見產品型錄:

http://www.icpdas.com/web/product/download/io\_and\_unit/local\_io/xv\_board/document/data\_sheet/XV308.pdf

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	XV308 Firmware[A102]							
	Configuration AI/AI Alarm DO/Alarm Host WDT DI About							
注意:	DO Status							
 若使用 <b>XV308</b> 需在								
"DO/Alarm" 頁籤中 ·	Set Power On							
不勾選仕何 DO 的情況ト,	Set Safe Value O Read Safe 與 "Set Safe Value" 按鈕。							
點選 "Set Power On" 與	Alarm Status							
"Set Safe Value" 按鈕。	H:0 H:1 H:2 H:3 H:4 H:5 H:6 H:7 Clear Clear Clear Clear Clear Clear Clear							
	L:0 L:1 L:2 L:3 L:4 L:5 L:6 L:7 Clear Clear Clear Clear Clear Clear Clear Clear							

7. 請切換到 "Configuration" 頁籤並點選 "Set Module Configuration" 按鈕 (步驟 5),即完成了 AI/AO 配置,請關閉 "DCON\_Utility\_Pro\_CE\_200.exe" 並點選桌面上的 "Win\_GRAF\_WP\_5238" 重新啟動 Win-GRAF Driver (步驟 1)。

XV310 - Analog Input: 可依照上述類似的步驟來規劃 XV310 的 AI/AO 通道。

Type Code	Range	Data Format	Minimum	Maximum
05		Engineering	-25000	+25000
05	+/-2.5 V	Hexadecimal	8000h	7FFFh
06	+/ 20 mA	Engineering	-20000	+20000
00	+/-20 MA	Hexadecimal	8000h	7FFFh
07	14 m A ~ 120 m A	Engineering	+4000	+20000
07	+4 MA +20 MA	Hexadecimal	0000h	FFFFh
00	. / 10. /	Engineering	-10000	+10000
08	+/-10 V	Hexadecimal	8000h	7FFFh
00		Engineering	-5000	+5000
09	+/-3 V	Hexadecimal	8000h	7FFFh
0.4	. / 1 \/	Engineering	-10000	+10000
UA	+/-1 V	Hexadecimal	8000h	7FFFh
00	1/20 mA	Engineering	-20000	+20000
UD	+/-20 MA	Hexadecimal	8000h	7FFFh
1 Δ	0 m 4 ~ + 20 m 4	Engineering	0	+20000
1A	0 MA +20 MA	Hexadecimal	0000h	FFFFh

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# <u>註:</u>

- 資料格式建議使用 "Engineering",較方便辨識數值。
   (例如: +/-2.5 V 顯示為 -25000~+25000, +4 mA~+20 mA 顯示為 +4000~+20000)
- 2. 若使用的 "Type Code" 為 0, 1, 06, 07, 0D, 1A 請注意板卡上 4 個 Jumper 的位置是否正確!

## 產品型錄:

http://www.icpdas.com/web/product/download/io\_and\_unit/local\_io/xv\_board/document/data\_sheet/XV310.pdf

## XV310 - Analog Output:

Type Code	Range	Data Format	Minimum	Maximum
0	0 m 4 ~ + 20 m 4	Engineering	0	+20000
0	0 MA +20 MA	Hexadecimal	0000h	FFFFh
1	14 m A ~ 1 20 m A	Engineering	+4000	+20000
T	+4 MA +20 MA	Hexadecimal	0000h	FFFFh
2	0\/~.10.\/	Engineering	0	+10000
Ζ	00 4 +10 0	Hexadecimal	0000h	FFFFh
2	1/ 10 1/	Engineering	-10000	+10000
5	+/-10 V	Hexadecimal	8000h	7FFFh
4	0.1/~ . E.1/	Engineering	0	+5000
4	0 0 + 5 0	Hexadecimal	0000h	FFFFh
Г		Engineering	-5000	+5000
5	+/-5 V	Hexadecimal	8000h	7FFFh

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#### 1.1.1. 連接 XV107, XV107A (8 DI, 8 DO)

XV107, XV107A 是一款具有 8 DI 與 8 DO 的擴充卡,本章節說明的 Win-GRAF 範例程式為 "demo XV107.zip",使用前請先參考 1.1 節 的 XV Board 使用說明。

## 範例說明:

此範例建立了 2 個 Data Block,一個用來讀取 8 個 DI 資料,另一個用來寫出 8 個 DO 資料。 1. 滑鼠雙擊第 1 個 Data Block (即,<2> Read Input Bits) 來開啟設定視窗。



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	_				1	_	

2. 滑鼠雙擊第 2 個 Data Block (即,<15> Write Coil Bits) 來開啟設定視窗。

RTU: CO	aster						17	Name		Туре
	M0:115200,N,	8,1						XV107_	DI_05	BOOL
	Read Input Bits	:(1)[18]				-		XV107	DI_06	BOOL
Image: Image	Write Coil Bits	(1)[18]						XV107_	DI_07	BOOL
	0: XV107_DO	_00					~	XV107	status	DINT
Symbol	1	Operation	N	Offset	Mask	Storage	-	XV107_	DO_00	BOOL
H XV107 DO 00		Data exch	Iange	0	FFFF	Default	_	XV107_	DO_01	BOOL
XV107 DO 01		Data exch	lange	1	FFFF	Default		XV107_	DO_02	BOOL
XV107 DO 02		Data exch	lange	2	FFFF	Default		XV107_	DO_03	BOOL
, XV107_D0_03		Data exch	ange	3	FFFF	Default		XV107_	DO_04	BOOL
* XV107_D0_04		Data exch	lange	4	FFFF	Default		XV107_	DO_05	BOOL
XV107_D0_05		Data exch	iange	5	FFFF	Default		XV107_	DO_06	BOOL
XV107_D0_06		Data exch	iange	6	FFFF	Default		< N/107	DO 07	POOL
XV107_D0_07		Data exch	lange	7	FFFF	Default	Na	me	Va	ue
<							>	ine	1 40	iuc.
ODBUS Master	Request					5	<			
Deervert										
Request	T			17		ОК	1			
Description:	-							1		
<u>S</u> lave/Unit:	1		Slave	設備	<b>(</b> 即	XV boa	rd)			
			的Ne	t-ID ,	固定	[為 "1"	•			
MODBUS Reques						ב עווי				
<5> Write sing	le coil bit			~						
<6> Write sing	le holding r	egister		(000)						
<15> Write Co	il Bits		_	V						
LATES WATE BO			「古山		李紫					
Data block			一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一一		貝心	t °				
Bace address:	1									
base <u>a</u> udress,	-									
<u>N</u> b items:	8		田位均	上1	開始	・寫出(	8 個	0		
Activation										
O Periodic:	a	ms	0							
			1							
Onrai			(on erro	or)				•		
On change			≫业才	コム緯	٤n±	ᆍᄚᆈ				
Misc.			貝州角	目以受	"时,	<b></b> / 為山	0			
	100	me								
Timeout:	100								<b>-</b>	
	1		芏 10	0 mc	半回	雁, <del>志</del>	二日	し いちょう しょう しょう しょう しょう しょう しょう しょう しょう しょう し		
Mb trials:	1.5		71 IU	0 1113		1111 121	1 7 7 7	[]]		
	XV107_D0_01 XV107_D0_02 XV107_D0_03 XV107_D0_04 XV107_D0_05 XV107_D0_06 XV107_D0_07 CODBUS Master Request Description: Slave/Unit: MODBUS Request Slave/Unit: MODBUS Request Code Write sing Code W	XV107_D0_01 XV107_D0_02 XV107_D0_03 XV107_D0_04 XV107_D0_05 XV107_D0_06 XV107_D0_07 X <b>CODBUS Master Request</b> <b>Request</b> <b>Description:</b> Slave/Unit: 1 <b>MODBUS Request</b> <5> Write single coil bit <5> Write single coil bit <5> Write single coil bit <5> Write single coil bit <5> Write single coil bit <15> Write Coil Bits <16> Write Nelding Posici <b>Data block</b> Base address: 1 Nb items: 8 Activation Periodic: 0 On call On change	XV107_D0_01       Data exch         XV107_D0_02       Data exch         XV107_D0_03       Data exch         XV107_D0_04       Data exch         XV107_D0_05       Data exch         XV107_D0_06       Data exch         XV107_D0_07       Data exch         Qescription:	XV107_D0_01       Data exchange         XV107_D0_02       Data exchange         XV107_D0_03       Data exchange         XV107_D0_04       Data exchange         XV107_D0_05       Data exchange         XV107_D0_06       Data exchange         XV107_D0_07       Data exchange         Y107_D0_07       Image: Sige         Y107_D0_07       Image: Sige         Y1	XV107_D0_01       Data exchange       1         XV107_D0_02       Data exchange       2         XV107_D0_03       Data exchange       3         XV107_D0_04       Data exchange       4         XV107_D0_05       Data exchange       5         XV107_D0_06       Data exchange       6         XV107_D0_07       Data exchange       6         XV107_D0_07       Data exchange       7         XV107_D0_07       Image       Slave         Yuter       Image       Slave       Yuter         Yuter       Image       Slave       Yuter	XV107_D0_01       Data exchange       1       FFFF         XV107_D0_02       Data exchange       2       FFFF         XV107_D0_03       Data exchange       3       FFFF         XV107_D0_06       Data exchange       4       FFFF         XV107_D0_06       Data exchange       5       FFFF         XV107_D0_07       Data exchange       7       FFFF         MODBUS Request       1       Slave       Big       Big         <5> Write single coil bit       <6> Write Single holding register       Slave       Risc       Risc         Data block       Base gddress:	X×107_D0_01       Data exchange       1       FFFF       Default         X×107_D0_02       Data exchange       2       FFFF       Default         X×107_D0_04       Data exchange       3       FFFF       Default         X×107_D0_05       Data exchange       4       FFFF       Default         X×107_D0_06       Data exchange       5       FFFF       Default         X×107_D0_07       Data exchange       7       FFFF       Default         Quest       O       OK       Quest       OK       Pefault         Quest       O       OK       Sute       OK       Pefault         Secription:       1       Slave       SUB(ID · KV boat       No         MODBUS Request       Image       Image       Image       Image       Image <td>W107_D0_01       Data exchange       1       FFFF       Default         W107_D0_02       Data exchange       2       FFFF       Default         W107_D0_03       Data exchange       3       FFFF       Default         W107_D0_05       Data exchange       4       FFFF       Default         W107_D0_06       Data exchange       5       FFFF       Default         W107_D0_06       Data exchange       5       FFFF       Default         XV107_D0_07       Data exchange       5       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         Request       OK       OK       Periodic:       0       OK         glave/Unit:       1       Slave       Bdf (ID · XV board)       No         MODBUS Request       I       Slave       Bdf (ID · XV board)       No         MODBUS Request       1       Slave       Bdf (ID · XV board)       No         Modeling Register       Slave       Bdf N       Slave       Slave       <t< td=""><td>W107_D0_01       Data exchange       1       FFFF       Default       XY107_XY107_D0_02         W107_D0_03       Data exchange       2       FFFF       Default       XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_05         W107_D0_05       Data exchange       4       FFFF       Default       YY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_06         W107_D0_05       Data exchange       6       FFFF       Default       FFFF       Default         XY107_D0_06       Data exchange       6       FFFF       Default       YY107_XY</td><td>XM107_D0_01       Data exchange       1       FFFF       Default       XM107_D0_03         XM107_D0_03       Data exchange       3       FFFF       Default       XM107_D0_03         XM107_D0_04       Data exchange       3       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_07         Name       Val       Val       Name       Val         CODBUS Master Request       OK       Name       Val         Periodic       0       OK       Name       Val         Slave/Unit:       1       Slave       Bdf (II) · XV board)       Name       Val         MODBUS Request       OK       Base gddress:       1       Slave       Bdf (II) · XV board)       Name       Slave         Vitie single holding register       Slave       Bal exchange       Slave       Bd exchange       Slave</td></t<></td>	W107_D0_01       Data exchange       1       FFFF       Default         W107_D0_02       Data exchange       2       FFFF       Default         W107_D0_03       Data exchange       3       FFFF       Default         W107_D0_05       Data exchange       4       FFFF       Default         W107_D0_06       Data exchange       5       FFFF       Default         W107_D0_06       Data exchange       5       FFFF       Default         XV107_D0_07       Data exchange       5       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         XV107_D0_07       Data exchange       7       FFFF       Default         Request       OK       OK       Periodic:       0       OK         glave/Unit:       1       Slave       Bdf (ID · XV board)       No         MODBUS Request       I       Slave       Bdf (ID · XV board)       No         MODBUS Request       1       Slave       Bdf (ID · XV board)       No         Modeling Register       Slave       Bdf N       Slave       Slave <t< td=""><td>W107_D0_01       Data exchange       1       FFFF       Default       XY107_XY107_D0_02         W107_D0_03       Data exchange       2       FFFF       Default       XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_05         W107_D0_05       Data exchange       4       FFFF       Default       YY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_06         W107_D0_05       Data exchange       6       FFFF       Default       FFFF       Default         XY107_D0_06       Data exchange       6       FFFF       Default       YY107_XY</td><td>XM107_D0_01       Data exchange       1       FFFF       Default       XM107_D0_03         XM107_D0_03       Data exchange       3       FFFF       Default       XM107_D0_03         XM107_D0_04       Data exchange       3       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_07         Name       Val       Val       Name       Val         CODBUS Master Request       OK       Name       Val         Periodic       0       OK       Name       Val         Slave/Unit:       1       Slave       Bdf (II) · XV board)       Name       Val         MODBUS Request       OK       Base gddress:       1       Slave       Bdf (II) · XV board)       Name       Slave         Vitie single holding register       Slave       Bal exchange       Slave       Bd exchange       Slave</td></t<>	W107_D0_01       Data exchange       1       FFFF       Default       XY107_XY107_D0_02         W107_D0_03       Data exchange       2       FFFF       Default       XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_05         W107_D0_05       Data exchange       4       FFFF       Default       YY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_XY107_D0_06         W107_D0_05       Data exchange       6       FFFF       Default       FFFF       Default         XY107_D0_06       Data exchange       6       FFFF       Default       YY107_XY	XM107_D0_01       Data exchange       1       FFFF       Default       XM107_D0_03         XM107_D0_03       Data exchange       3       FFFF       Default       XM107_D0_03         XM107_D0_04       Data exchange       3       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_06       Data exchange       5       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_06         XM107_D0_07       Data exchange       6       FFFF       Default       XM107_D0_07         Name       Val       Val       Name       Val         CODBUS Master Request       OK       Name       Val         Periodic       0       OK       Name       Val         Slave/Unit:       1       Slave       Bdf (II) · XV board)       Name       Val         MODBUS Request       OK       Base gddress:       1       Slave       Bdf (II) · XV board)       Name       Slave         Vitie single holding register       Slave       Bal exchange       Slave       Bd exchange       Slave



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#### 1.1.3. 連接 XV111, XV111A (16 DO)

XV111, XV111A 是一款具有 16 DO 的擴充卡,本章節的範例程式為 "demo\_XV111.zip",使用前 請先參考 1.1 節 的 XV Board 使用說明。

#### 範例說明:

此範例建立了 2 個 Data Block,一個用來寫出 16 個 DO 資料,另一個用來讀取 DO 狀態。 1. 滑鼠雙擊第 1 個 Data Block (即,<15> Write Coil Bits) 來開啟設定視窗。



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2.	2 個 Data Bloc ams ugging) ration	k (印 · <1) DBUS Master RTU: COM0:1152 *■ <15> Write Cr *■ <15> Write Cr *■ <15 Read Co L · C · C · C · C · C · C · C · C · C ·	> Read Co 200,N,8,1 oil Bits (1) [116 il Bits (1) [11] rt: XV111_statu Uperatio Error repo	oil Bits) 來 。 n Off n Off	開啟設定視窗 set Mask Storag FFFF Default	xv111 xv111 xv111 xv111 xv111 xv111 xv111 xv111 xv111	Type           D0_10         BOOL           D0_11         BOOL           D0_12         BOOL           D0_13         BOOL           D0_14         BOOL           D0_15         BOOL           status         DINT
<mark>注意:</mark> 若 "Or 設定為	oeration"設定 "0"。 MODBUS M Request Descripti	為 "Error laster Requ	report"	· 該變數 (〕	資料型態: DIN	NT) 的 "Offs	set"值需
	<u>Slave/Un</u> MODBUS F <u>&lt;1&gt; Re</u> <2> Re <3> Re	it: 1 tequest ad Coil Bits ad Input Bits ad Holding Re	gisters	Slave 設住 的 Net-ID	備 (即・XV b )・固定為 ": 狀態 。	oard) 1" °	
	Data block Base add <u>N</u> b items	iress: 1		由位址 1	開始,讀取	【1個。	
	Activation Period On ca On ch Misc. <u>T</u> imeout:	lic: 50 Ji ange	ms	0 (on error) 每 50 ms	讀取一次。		-
	Nb trials:	1		若 100 m	s 未回應,ā	表示異常。	
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<b>)</b> 冯臼雔墼笋	2 個 Data Block	/即,/19	Nrita Cail Bi	tc)	的铅宁泪窗。		



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#### 1.1.5. 連接 XV308 (8 AI, 8 DIO)

XV308 是一款具有 8 AI 與 8 DIO (即·DI+DO=8) 的擴充卡·本章節說明的 Win-GRAF 範例程式 為 "demo\_XV308\_1.zip", "demo\_XV308\_2.zip", "demo\_XV308\_3.zip", 使用前請先參考 <u>1.1 節</u> 的 XV Board 使用說明,並預先在 PAC 內使用 "DCON\_Utility\_Pro\_CE\_200.exe" 規劃 AI 通道。

### 範例說明:(demo\_XV308\_1)

此範例建立了 2 個 Data Block · 一個用來讀取 8 個 AI 資料 · 另一個用來讀取 8 個 DI 資料 · 1. 滑鼠雙擊第 1 個 Data Block (即 · <4> Read Input Registers) 來開啟設定視窗 ·



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2. 滑鼠雙擊第 Workspace	2 個 Data Block	、(即・<2) ODBUS Master	> Read In	put Bits) 來	開啟設	定視窗	•	
Exception programs     Programs     Main     Watch (for det     Main     Soft Scop     Initial values     Global defines     Variables     Types	grams bugging) ie iuration iuration ie iuration ie ie ie ie ie ie ie ie	RTU: COM0:1 "■ <4> Read "■ <2> Read _0 _1 _2 _3 _4 _5 _6 _7	15200,N.8,1 Input Registers Input Bits (1) (3 Oper Data Data Data Data Data Data Data	(1) [18] 3.40] ation exchange 0 exchange 1 exchange 3 exchange 4 exchange 5 exchange 6 exchange 7	Offset Mas D FFFF 2 FFFF 3 FFFF 5 FFFF 6 FFFF 7 FFFF	k Storage Default Default Default Default Default Default Default	<ul> <li>X/308_</li> </ul>	AI_7 INT status DINT DI_0 BOOL DI_1 BOOL DI_2 BOOL DI_3 BOOL DI_3 BOOL DI_4 BOOL DI_5 BOOL DI_5 BOOL DI_6 BOOL DI_7 BOOL
<u>注意:</u> 讀取 xv3	308的DI資料 MODBUSM Request Descriptio	時,位址 aster Requ	t必需由 est	"33" 開始。		ок		
	Slave/Uni	t: 1 equest		Slave 設備 的 Net-ID	「(即・ ・固定	XV board 為 "1"。	d)	
	<1> Rea <2> Rea <3> Rea <4> Dea	ad Coil Bits ad Input Bits ad Holding Re ad Ioput Booi	egisters	 讀取 DI 貣	資料。	]		
	Data block Base add	recc <sup>,</sup> 33	-			•		
	<u>N</u> b items:	8		由位址 <b>33</b>	開始	·讀取 8	3個。	
	Activation	lic: 50	ms	0				
	O On ch	ange		每 50 ms	讀取一	次。		
	Misc. Timeout:	100	ms					
	Nb trials:	1		若 100 ms	未回應	)、表示	、異常。	
							-	
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<b>範例說明:</b> (dem 此範例建立了 2	no_XV308_2) 2 個 Data Block	,一個用	來讀取 8 個	]AI 資料	· 另一個用來	∝寫出 8 個	DO 資料。
1.	1 個 Data Bloc	k (即,<4:	Read Input	Registers)	來開啟設定	視窗。	
Workspace demo_XV308_2 Frograms Main Watch (for de Main Initial values Global defines Types	10 Drivers       Image: symbol pe       guration sing       s       Symbol Pe       Image: symbol Pe <t< td=""><td>40DBUS Master ♣ RTU: COM0:1 ■ • • ● &lt;4&gt; Read • • • ● &lt;15&gt; Writ • 10 • 11 • 12 • 12 • 13 • 14 • 15 • 16 • 17 • 14 • 18 • 19 • 19</td><td>15200,N,8,1 Input Registers (1) [1. e Coil Bits (1) [18] Operation Data exchar Data exchar</td><td>8) nge 0 nge 1 nge 2 nge 3 nge 4 nge 5 nge 6 nge 6 nge 7 0</td><td>set Mask Storag FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default</td><td>Name         Image: Name         Image: Name</td><td>Type obal variables AL_0 INT AL_1 INT AL_2 INT AL_2 INT AL_2 INT AL_3 INT AL_4 INT AL_5 INT AL_6 INT AL_6 INT AL_7 INT status DINT</td></t<>	40DBUS Master ♣ RTU: COM0:1 ■ • • ● <4> Read • • • ● <15> Writ • 10 • 11 • 12 • 12 • 13 • 14 • 15 • 16 • 17 • 14 • 18 • 19 • 19	15200,N,8,1 Input Registers (1) [1. e Coil Bits (1) [18] Operation Data exchar Data exchar	8) nge 0 nge 1 nge 2 nge 3 nge 4 nge 5 nge 6 nge 6 nge 7 0	set Mask Storag FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default	Name         Image: Name	Type obal variables AL_0 INT AL_1 INT AL_2 INT AL_2 INT AL_2 INT AL_3 INT AL_4 INT AL_5 INT AL_6 INT AL_6 INT AL_7 INT status DINT
<u>注意:</u> "Offset" 位址。若 設定為"	的值是由"O" "Operation" O"。 MODBU	開始,而 没定為"E <mark>S Master R</mark> e st	"Offset"值 rror report" equest	加 1 (Base ,該變數(	e address) 才 (資料型態: Dl	是該變數的 NT) 的"Off IIII	Modbus <sup>f</sup> set"值需
		e/Unit: 1		Slave 設住 的 Net-ID	備 (即→XV bo → 固定為 "1	oard) ." °	
		<ul> <li>Read Input I</li> <li>Read Holding</li> <li>Read Input I</li> <li>Write cipale</li> <li>Write cipale</li> </ul>	Bits g Registers Registers	<b>論</b> 取 AI 5	資料。		
	Base <u>N</u> b it	ems: 8		由位址 1	開始,讀取	8個°	
		ition eriodic: 5 n call n change	i0 ms	<sup>0</sup> 每 50 ms	讀取一次。		
	Misc. <u>T</u> ime Nb tr	out: 1 rials: 1	.00 ms	若 100 m	s 未回應,衰	長示異常。	]
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demo_XV308_2      Exception programs     Programs     Main	transmission of the second								
🗄 🦳 Exception programs		6 Master				~	🝸 Name	9	Туре
🖻 🛄 Programs	A D A RTU:	COM0:115200,N,8,1					XV3	308_status	DINT
🔛 🏦 Main	*******	4> Read Input Registers	s (1) [18]				XV3	308_DO_0	BOOL
13 A 34 34 34 34 34 34 34 34 34 34 34 34 34		15> Write Coil Bits (1) [1	8]				XV3	308_DO_1	BOOL
🚖 🔤 Watch (for debugging)	Cumbel		untion .	04	Mad	Charles	± XV3	308_DO_2	BOOL
Soft Scope	Symbol	Uper	ration	Uffset	Mask	Defect	- XV3	308_DO_3	BOOL
📰 Initial values	XV308_DU_0	Data	exchange	0	FFFF	Derault	XV3	308 DO 4	BOOL
	XV308_D0_1	Data	exchange	1	FFFF	Default	XV3	308 DO 5	BOOL
🚽 🕺 🛛 Global defines	XV308_D0_2	Data	exchange	2	FFFF	Default	XV3	308 DO 6	BOOL
Variables	XV308_D0_3	Data	exchange	3	FFFF	Default	XV3	308 DO 7	BOOL
	I ×V308_D0_4	Data	exchange	4	FFFF	Default			
	XV308_D0_5	Data	exchange	5	FFFF	Default		J.	
	XV308_D0_6	Data	exchange	6	FFFF	Default	Name	V	'alue
	XV308_D0_7	Data	exchange	7	FFFF	Default			
	<						> <		
2	IODBUS Master	Request							
	Request				0				
	Description:			l	OK		-		
	<u>S</u> lave/Unit:	1	Slave 設	備(即	] · X\	/ board)	)		
	MODBUS Reques	it -	的 Net-I	D,固	定為	"1" °			
	<5> Write sin <6> Write sin <15> Write Co >12 \ Write Do	gle holding register bil Bits bilding Register	宮出 D〇	容彩					
	Data block				'				
	Base <u>a</u> ddress:	1							
	<u>N</u> b items:	8	由位址:	1 開始	台・寫	四出 8 伯	固。		
	Activation								
	Operiodic:	0 ms	0						
	Ooncaji		(on error)				_		
	Misc		資料有改	て變時	,才	寫出。			
	Timeout:	100 ms						_	
			±± 100 m	~ ±	同確	,丰元月	24章。		

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<b>範例說明</b> :(dem 此範例建立了 3 資料・第 3 個僅	io_XV308_3) 個 Data Block 堇用來讀取 4 们	·第1 ( 固 DI 資料	固用來讀取 8 <sup>科。</sup>	個 AI 資	译料·第2個	」 同來寫出 4	4 個 DO
1. 滑鼠雙擊第	1 個 Data Block	< (即・<4)	> Read Input R	egisters)	來開啟設定社	見窗。	
<ul> <li>demo_XV308_3</li> <li>Exception prog</li> <li>Programs</li> <li>Main</li> <li>Watch (for debinant of the second of the</li></ul>	rams ugging) Symbol Sym	RTU: COM0:1 "■ <4> Read "■ <15> Write "■ <2> Read _0 _1 _2 _3 _4 _5 _6 _7	15200,N,8,1 Input Registers (1) [18] Coil Bits (1) [14] Input Bits (1) [3340] Data exchange Data exchange Data exchange Data exchange Data exchange Data exchange Data exchange Data exchange Data exchange Data exchange	1 0 1 2 3 4 5 6 7	et Mask Storage FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default FFFF Default	<ul> <li>Name</li> <li>X&lt;308_/</li> <li< td=""><td>Type           Al_1         INT           Al_2         INT           Al_3         INT           Al_4         INT           Al_5         INT           Al_6         INT           Al_7         INT           Al_6         INT           Al_6         INT           Al_7         INT           Status         DINT           DO_0         BOOL           DO_1         BOOL           Value         Value</td></li<></ul>	Type           Al_1         INT           Al_2         INT           Al_3         INT           Al_4         INT           Al_5         INT           Al_6         INT           Al_7         INT           Al_6         INT           Al_6         INT           Al_7         INT           Status         DINT           DO_0         BOOL           DO_1         BOOL           Value         Value
<mark>注意:</mark> "Offset"的 位址。	的值是由"0" MODBUS M Request Descripti Slave/Un MODBUS F	開始 · 而 faster Regr on: it: 1	"Offset"值加 lest	]1 (Base /e 設備 Net-ID,	address) 才 区 (即・XV boar 固定為 "1"。	是該變數的 fd)	Modbus
	Caller Control Cont	ad Input Bits ad Holding Re ad Input Reg its cipals cail tress: 1 : 8	egisters isters ister iter iter iter iter	取 AI 資 立址 1 厚	料。 乳始,讀取 8	(個。	
	<ul> <li>○ Period</li> <li>○ On ca</li> <li>○ On ch</li> <li>Misc.</li> <li>Timeout:</li> <li>Nb trials:</li> </ul>	dic: 50 Il ange 100 1	ms 0 每	50 ms 請 100 ms	東一次。 未回應,表示	、異常。	
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<mark>注意:</mark> 讀取 XV303 資料時 · 位 由 "33" 開	8 的 DI Σ址必需 始。	CODBUS Maste         Request         Description:         Slave/Unit:         MODBUS Require         <1> Read Ci         <2> Read If         <3> Read Hi         <4> Read If         <3> Read Hi         <4> Read If         <3> Read If      <	er Request	Slave 設 Net-ID・ 讀取 [ 由位址 但此例 <sup>4</sup> 每 50 m 若 100 r	<ul> <li>○K</li> <li>○K</li> <li>○G</li> <li>○G</li></ul>	<ul> <li>▲</li> <li>→</li> <li>→</li></ul>	·

#### 1.1.6. 連接 XV310 (4 AI, 2 AO, 4 DI, 4 DO)

XV310 是一款具有 4 AI、2 AO、4 DI 與 4 DO 的擴充卡,本章節說明的 Win-GRAF 範例程式為 "demo\_XV310.zip",使用前請先參考 <u>1.1 節</u> 的 XV Board 使用說明,並預先在 PAC 內使用 "DCON\_Utility\_Pro\_CE\_200.exe" 規劃 AI/AO 通道。

## 範例說明:

此範例建立了 4 個 Data Block · 第 1 個用來讀取 4 個 AI 資料 · 第 2 個用來寫出 4 個 DO 資 料 · 第 3 個用來讀取 4 個 DI 資料 · 第 4 個用來寫出 2 個 AO 資料 ·

1. 滑鼠雙擊第 1 個 Data Block (即, <4> Read Input Registers) 來開啟設定視窗。

Workspace	101	Drivers								ΗZ	X
demo_XV310      Exception programs     Programs     Main     Watch (for debugging)     Soft Scope		M     MODBUS Mask	er D:115200,N,8,1 ad Input Registers (1) [14] /rite Coil Bits (1) [14] ad Input Bits (1) [3336] /rite Holding Registers (1) [3334]	-(1				✓ Name ✓ Name ✓ XV310 XV310 XV310 XV310 XV310 XV310 XV310	Global va )_AI_0 )_AI_1 )_AI_2 )_AI_3	Type riables INT INT INT INT	<ul> <li></li></ul>
Initial values 	¢;;>	Symbol XV310_AI_0 XV310_AI_1 XV310_AI_2 XV310_AI_2 XV310_AI_3	Operation Data exchange Data exchange Data exchange Data exchange Data exchange	Offset 0 1 2 3	Mask FFFF FFFF FFFF FFFF	Storage Default Default Default Default		XV310 XV310 XV310 XV310 XV310	)_AO_0 )_AO_1 )_status )_DO_0	INT INT DINT BOOL	
		XV310_status	Error report	0 Docum	hent	Default	>	Name <		/alue	>

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<u>注意:</u> "Offset" é 位址。若 需設定為	的值是由"0"開 "Operation"設筑 "0"。	始,而"C E為"Erro	Offset"值加 r report",	]1 (Base 該變數(	address) 才 資料型態: Dl	是該變數的 NT) 的 "Off	Modbus set"值
	MODBUS Ma	ster Request					
	Request Description				ОК		
	<u>S</u> lave/Unit:	1	Slav	ve 設備 Net-ID,	(即 <sup>,</sup> XV boar 固定為 "1"。	d)	
	<pre>MODBUS Red &lt;2&gt; Read &lt;3&gt; Read</pre>	juest Input Bits Holding Regist	rers				
	<4> Read	Input Registe	is is 讀I	又 AI 資	料。		
	Data block Base <u>a</u> ddre	ss: 1					
	<u>N</u> b items:	4		立址 1 厚	閉始・讀取 4	個。	
	Activation Periodic On call On char	50 ge	ms 0 每	50 ms 謮	責取一次。		
	Misc. <u>T</u> imeout:	100	ms				
	Nb trials:	1	若	100 ms	未回應・表示	₹異常・	
<ol> <li>3. 滑鼠雙擊第 2</li> </ol>	2 個 Data Block (	们,<15> \	Write Coil B	ts) 來查	看設定視窗。		
demo_XV310		US Master	ON 91			▲ ▼ <u>Name</u>	
Programs		4> Read Input <15> Write Coll	Registers (1) [14] Bits (1) [14]	2		XV310_ XV310_	status DINT
Watch (for deb	ugging)	<2> Read Input <16> Write Hold	Bits (1) [3336]	1 141		XV310_ XV310_ XV310	DO_1 BOOL
Initial values	ration		Operation	Offs	et Mask Storage	XV310_ XV310	DO_3 BOOL
j Binding coninge g Global defines	×V310_D0_0		Data exchange Data exchange	0 1	FFFF Default FFFF Default	XV310_ XV310_	
	₽ ×V310_D0_2 ×V310_D0_3		Data exchange Data exchange	2 3	FFFF Default FFFF Default	Name	Value
	*			, ( <u>198</u>		> <	

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Classification	Win-GRAF Chi	nese FAQ	-010							
Author	Janice Hong	Version	1.0.0	Date	2016	, 01	Page	23 / 25		
	MODBU	S Master Re	quest				<			
	Reque	st								
	Desc	ription:				OK				
	Slave	/Unit: 1		Classa =/		VA / I= -				
	MODE		~	Slave 武	四月(四)。	500 VX 11411 世	iia)			
		Write single o	oil bit	」的 Net-ID, 回 正 為 "1"。						
	<5> Write single coll bit <6> Write single holding register <15> Write Coll Bits									
	寫出 Do	Ŋ 資料。								
	Data b	lock								
	Base	address: 1								
	<u>N</u> b ite	ems: 4		由位址	1 開始	· 寫出 4	4 個。			
	Actival	tion								
		eriodic: 0	ms	0						
	() ()	n change 👝 🗕		(on error)						
	Misc.			資料有改變時,才寫出。						
	Time	out: 1	00 ms					-		
	Nb tr	ials: 1		若 100	ms 未回	應・表	示異常。			
								-		
3.	3 個 Data Bloc	k (即・<2)	> Read Inp	ut Bits) 來	查看設定	E視窗。				
Workspace	IO Drivers *									
e demo_XV310		10DBUS Master	15200 N 9 1				Name			
Programs			Input Registers (1)	[14]	3		XV31	0_DO_1 BOOL		
🔲 🏥 Main 🖃 🦳 🛄 Watch (for det	ugging)	]*∎ <15> Write ]*∎ <2> Read	e Coil Bits (1) [14] Input Bits (1) [33	36]			XV31 XV31	0_DO_2 BOOL 0 DO 3 BOOL		
Soft Scop	e 📕	∎* <b>∎</b> <16> Write	e Holding Register:	s <mark> (</mark> [3334]			× XV31			
🛄 Initial Values	uration Symbol	10	Operatio	on l	Offset Mask	Storage	XV31 XV31	0_DI_1 BOOL 0_DI_2 BOOL		
🦰 😽 Global defines	مراجة ××310_L	n_0 01_1	Data exc Data exc	hange	1 FFFF	Default	XV31	0_DI_3 BOOL 🚽		
E Types	₽ XV310_0	)I_2	Data exc	hange :	2 FFFF	Default	Namo	Value		
	×v3IU_L	/_3				Derault	> <	Value		
				obnical	L					
		ICP DAS	CO., LTO. 16	conical Do	cument					

Author       Janice Hong       Version       1.0.0       Date       2016, 01       Page       24 / 25         MODBUS Master Request       Image: State State       Image: Sta	Classification	Win-GRAF Chi	nese FAQ-	010				
WODBUS Master Request         Pequest       CK         Pequest       CK         Pequest       Slave 80/ft (II) · XV board)         的 Net-ID · 固定為 "1" ·         C> Peed Col Bts         Period:       Figurest         Base address:       33         Base address:       34         H '33"       Byb         Hot blob       See address:         See address:       34         Hot blob       See address:         See address:       34         Hot blob       See address:         See address:       34         Hot blob       See address:         See address:       35         Hot blob       See address:         See address:       35         Hot blob       See address:         Hot blob       See address: <td< td=""><td>Author</td><td>Janice Hong</td><td>Version</td><td>1.0.0</td><td>Date</td><td>2016, 01</td><td>Page</td><td>24 / 25</td></td<>	Author	Janice Hong	Version	1.0.0	Date	2016, 01	Page	24 / 25
Workspace       0       Unders*       Image: Solution of the solutio	<u>注意:</u> 讀 DI 資料時 由 "33" 開 4.	取 XV310 的 · 位址必需 始。 4 個 Data Bloc	CODBUS Mas Request Description: Slave/Unit: MODBUS Req <1> Read <2> Read <2 No trials: <2 Read <2 Re	ter Request	Slave 的 Ne	設備(即·) 設備(即·) et-ID·固定為 DI資料。 L 33 開始・ s 讀取一次。 0 ms 未回應 ers)。	▲ (V board) ③ "1"。 請取 4 個	
	Vorkspace demo_XV310 Exception prog Programs Main Main Variables Global defines Variables Types	Jrams	40DBUS Master	15200,N,8,1 Input Registers (1) [14] Coil Bits (1) [336] Holding Registers (1) [3 Operation Data exchange Data exchange	334]	4 set Mask Storag FFFF Default FFFF Default	Name XV310 XV310 XV310 XV310 XV310 XV310 XV310 XV310 XV310	AL 1 INT AL 2 INT AL 2 INT AL 3 INT AO 0 INT AO 1 INT status DINT Value

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Classification		Vorsion	1.0.0	Data	2016 01	Daga	
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Author 注意: 寫入 AO 資料時, 由 "33" 開始	Janice Hong	Version DBUS Maste Request Description: Slave/Unit: MODBUS Reque <6> Write Sir <15> Write C <16> Write F <16> Write F Data block Base address: Mb items: Activation Periodic: On call On call On change Misc. Timeout: Nb trials: FAQ: a/index.php	1.0.0 r Request 1 r Request 1 1 1 st idding Registers idding Registers 1 1 1 1 1 1 1 1 1 1 1 1 1	Date Date Slave 設 的 Net-	2016, 01	Page board) "1"。 → 2 個。 → 表示異常。	25 / 25