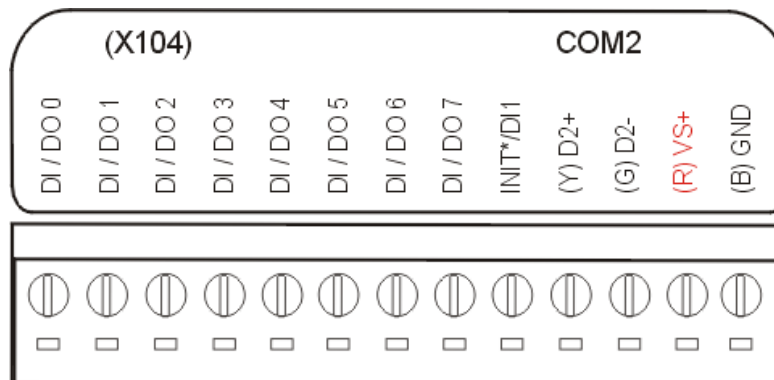

X104 Release Note:

Supported Products: I-7188XC

Specifications:

Digital Input / Output	
Channel	8
Input / Output	each channel can be programmed to D/I or D/O
Intra-module Isolation, Field to Logic	non-isolated
Note: Provide functions of DI and DO from CPU.	

Pin Assignment:



Jumper Setting:

Note: user should remove R19, R20, R21, R22 and R23 in the 7188XC first.

- J1: I/O expansion bus, connect to J1 of 7188XC
- J2: I/O expansion bus, connect to J2 of 7188XC
- J7: Three channels of 8 D/I/O
- TP0: Original function on 7188XC
- TP1: Five channels of 8 D/I/O
- Jumper: Select TP0 or TP1 function
 - TP0: J9 and J8 closed
 - TP1: J9 and J10 closed

Programming:

for I-7188XC

http://ftp.icpdas.com/pub/cd/8000cd/napdos/7188xabc/7188xc/demo/bc_tc/xboard/

Manual:

<http://ftp.icpdas.com/pub/cd/8000cd/napdos/xboard/document/>

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