

PISO-C64

64-Channel Optically Isolated Open-Collector Digital Output Board



Functional Description

The PISO-C64 is designed for use in PCI-Bus computers. The PISO-C64 has 64 channels of optically isolated digital outputs, arranged into four isolated banks. Each digital output offers a darlington transistor and integral suppression diode for inductive load. All isolated output channels are designed into group A, B, C and group D. The power supply for the output port should use the external power. The board interface to field logic signals, eliminating ground-loop problems and isolating the host computer from damaging voltages. The PISO-C64 has one 37-pin D-Sub connector and one 40-pin male header. The 40-pin to DB-37 flat-cable is used to fix with the case. The user can connect the digital signal through the second D-Sub connector. Each D-Sub connector contains 32 output channels.

Features

n 64-channel optically isolated digital output /open collector n Four isolated bank. n 3000V DC isolation voltage

Applications

n Factory automation n Product test n Laboratory automation

Specifications

n Isolation Output n Isolated open-collector: 125mA / each channel n External voltage: 30V (Max.) n Response time: 10KHz (Typical)

n Power consumption: +5V / 800mA

Environmental

- n Operating Temperature: 0 to 50°C
- n Storage Temp.: -20°C to 70 °C
- n Humidity: 0 to 90 % non-condensing
- n Dimension: 180 mm x 105 mm

Software

- n PISO-DIO Development Toolkit for DOS
- n PISO-DIO Development Toolkit for Win95
- n PISO-DIO Development Toolkit for WinNT

Order Description

n PISO-C64: 64-channel isolated digital outputs board

Options

- n DB-37: Directly connect signals to the back of PISO-C64
- n DN-37: I/O connector block with DIN-rail mounting and 37-pin D-Sub connector
- n PISO-DIO LabVIEW Development Toolkit for Win95
- n PISO-DIO LabVIEW Development Toolkit for WinNT

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CH16-31(+) DO_31 DO_30 DO_29 DO_28 DO_27	37 36 35 34 33 32		19 18 17 16 15 14 13	N.C. CH0-15(+) DO_15 DO_14 DO_13 DO_12 DO_11
DO_26	31		12	DO_10
DO_25 DO_24	29		11	DO_9
DO_23	28		09	DO_8 DO 7
DO_22 DO_21	27	•	08	DO_6
DO_20	25		07	DO_5
DO_19	24		05	DO_4 DO_3
DO_18 DO_17	23 22		04	DO_2
DO_16	21	•	03	DO_1
CH16-31(-)	20	• •	01	CH0-15(-)
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Pin Assignment

Note:

CH0-15(+): Power input +

CH0-15(-): Power input -

CH16-31(+): Power input +

CH16-31(-): Power input -